|   |                     |                      | UNITED STATES DEPAR<br>United States Patent and<br>Address: COMMISSIONER F<br>P.O. Box 1450<br>Alexandria, Virginia 223<br>www.uspto.gov | Trademark Office<br>OR PATENTS |
|---|---------------------|----------------------|--|--------------------------------|
| APPLICATION NO.   | FILING DATE         | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.  | CONFIRMATION NO                |
| 10/600,378  | 06/20/2003          | Hao-Cheng Lin        | B-5137 621043-3  | 8508                           |
| 36716 7   | 590 03/25/2005      |                      | EXAM   | INER                           |
| LADAS & PARRY   |                     |                      | VERDIER, CHRISTOPHER M   |                                |
| 5670 WILSHIRE BOULEVARD, SUITE 2100<br>LOS ANGELES, CA 90036-5679 |                     | ART UNIT             | PAPER NUMBER   |                                |
|   | 2, 211 / 2000 001 / |                      | . 3745   |                                |
|   |                     |                      | DATE MAILED: 03/25/2005  |                                |

Please find below and/or attached an Office communication concerning this application or proceeding.

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|   | Application No.   | Applicant(s)   |      |
|---|---|--|------|
|   | 10/600,378  | LIN, HAO-CHENG   | ED   |
| Office Action Summary   | Examiner  | Art Unit   |      |
|   | Christopher Verdier   | 3745   |      |
| The MAILING DATE of this communication a<br>Period for Reply  | appears on the cover sheet w  | ith the correspondence address   |      |
| A SHORTENED STATUTORY PERIOD FOR REF<br>THE MAILING DATE OF THIS COMMUNICATION<br>- Extensions of time may be available under the provisions of 37 CFR<br>after SIX (6) MONTHS from the mailing date of this communication.<br>- If the period for reply specified above is less than thirty (30) days, a<br>- If NO period for reply is specified above, the maximum statutory peri<br>- Failure to reply within the set or extended period for reply will, by sta<br>Any reply received by the Office later than three months after the ma<br>earned patent term adjustment. See 37 CFR 1.704(b). | N.<br>1.136(a). In no event, however, may a r<br>reply within the statutory minimum of thin<br>iod will apply and will expire SIX (6) MON<br>tute, cause the application to become AE | eply be timely filed<br>ty (30) days will be considered timely.<br>ITHS from the mailing date of this communicati<br>BANDONED (35 U.S.C. § 133). | ion. |
| Status  |   |  |      |
| 1) Responsive to communication(s) filed on  | ;   |  |      |
|   | his action is non-final.  |  |      |
| 3) Since this application is in condition for allow   | wance except for formal matt  | ers, prosecution as to the merits  | is   |
| closed in accordance with the practice unde   | er <i>Ex parte Quayle</i> , 1935 C.D  | ). 11, 453 O.G. 213.   |      |
| Disposition of Claims   |   |  |      |
| 4) Claim(s) <u>1-4</u> is/are pending in the applicatio   | n.  |  |      |
| 4a) Of the above claim(s) is/are withd  | Irawn from consideration.   |  |      |
| 5) Claim(s) is/are allowed.   |   |  |      |
| 6) Claim(s) <u>1-4</u> is/are rejected.   |   |  |      |
| 7) Claim(s) is/are objected to.   |   |  |      |
| 8) Claim(s) are subject to restriction and  | d/or election requirement.  |  |      |
| Application Papers  |   |  |      |
| 9) The specification is objected to by the Exam   | iner.   |  |      |
| 10) The drawing(s) filed on <u>20 June 2003</u> is/are:   | a) 🗍 accepted or b) 🛛 obje  | cted to by the Examiner.   |      |
| Applicant may not request that any objection to t   | he drawing(s) be held in abeyar   | nce. See 37 CFR 1.85(a).   |      |
| Replacement drawing sheet(s) including the corr   | -   |  | (d). |
| 11) The oath or declaration is objected to by the   | Examiner. Note the attached   | d Office Action or form PTO-152.   |      |
| Priority under 35 U.S.C. § 119  |   |  |      |
| 12) Acknowledgment is made of a claim for forei<br>a) All b) Some * c) None of:   | ign priority under 35 U.S.C. §  | § 119(a)-(d) or (f).   |      |
| 1. Certified copies of the priority docume  | ents have been received.  |  |      |
| 2. Certified copies of the priority docume  | ents have been received in A  | pplication No  |      |
| 3. Copies of the certified copies of the p  | riority documents have been   | received in this National Stage  |      |
| application from the International Bur  | eau (PCT Rule 17.2(a)).   |  |      |
| * See the attached detailed Office action for a l   | list of the certified copies not  | received.  |      |
|   |   |  |      |
| Attachment(s)   |   |  |      |
| <ol> <li>1) X Notice of References Cited (PTO-892)</li> <li>2) Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> </ol>  |   | Summary (PTO-413)<br>s)/Mail Date  |      |
| <ol> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/<br/>Paper No(s)/Mail Date</li> </ol>  |   | nformal Patent Application (PTO-152)   |      |
| U.S. Patent and Trademark Office  |   |  |      |

## Drawings

Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1 and 4 are rejected under 35 U.S.C. 102(e) as being anticipated by Hu 6,790,003. Note the heat-dissipating fan device comprising a fan housing 1 having a base wall 10, and a surrounding wall 1 extending from the base wall, a circuit board 21 disposed in the fan housing and mounted on the base wall, a fan impeller mounted on the circuit board and rotatable about a central axis that is transverse to the circuit board, with the fan impeller being formed with a plurality of radial fan blades 20, and a plurality of light emitting diodes 3 surface-mounted on the

circuit board and arranged around the fan impeller. The fan blades are made of a transparent material.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 2-3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hu 6,790,003 in view of Tang 6,741,042. Hu discloses a heat-dissipating fan substantially as claimed as set forth above, including a circuit board 21, but does not disclose a control unit mounted on the circuit board and coupled electrically to the light emitting diodes so as to control activation of the light emitting diodes, with the control unit being configured to control activation of the light emitting diodes in accordance with operating voltage from a power source.

Tang shows a light emitting device having a control unit 10 mounted on a circuit board 50 and coupled electrically to light emitting diodes 20 so as to control activation of the light emitting diodes, with the control unit being configured to control activation of the light emitting diodes in accordance with operating voltage from a power source (column 2, lines 18-30), for the purpose of forming a compact, integrated controlling assembly for the light emitting diodes.

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to form the fan of Hu such that a control unit is mounted on the circuit board and coupled electrically to the light emitting diodes so as to control activation of the light emitting diodes, with the control unit being configured to control activation of the light emitting diodes in accordance with operating voltage from a power source, as taught by Tang, for the purpose of forming a compact, integrated controlling assembly for the light emitting diodes.

## **Prior** Art

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Tseng is cited to show a fan with light emitting diodes, a circuit board, and an integrated circuit control unit on the circuit board.

Blissett is cited to show a fan with light emitting diodes, an integrated circuit board, and a microcontroller.

Ridge is cited to show a lamp with an impeller, and light emitting diodes mounted on a base.

Huang is cited to show an impeller with light emitting diodes.

Lin is cited to show a fan housing with light emitting diodes mounted thereto.

Tsai and Willis are cited to show light emitting diodes with printed circuit boards and controllers thereon.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher Verdier whose telephone number is (571) 272-4824. The examiner can normally be reached on Monday-Friday from 10:00-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward K. Look can be reached on (571) 272-4820. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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C.V. March 17, 2005 Christopher Verdier Primary Examiner Art Unit 3745